

Search Terms	
1	ANALOG
2	ANALOGS
3	ANALOGUE
4	ANALOGUES
5	CONTROL
6	CONTROLLED
7	CONTROLLEDS
8	CONTROLLER
9	CONTROLLERS
10	CONTROLLING
11	CONTROLLINGS
12	CONTROLS
13	ENABLE
14	ENABLER
15	ENABLERS
16	ENABLES
17	ENABLING
18	ENABLINGS
19	FLIP
20	FLIP-FLOP
21	FLIP-FLOPS
22	FLIPS
23	FLOP
24	FLOPS
25	MEASURE
26	MEASURED
27	MEASUREDS
28	MEASURES
29	MEASURING
30	MEASURINGS
31	MODULAR
32	MODULARS
33	MODULE

	<b>Total</b>	<b>US-PGRUB</b>	<b>USPAT</b>	<b>USOCR</b>	<b>EPO</b>	<b>JPO</b>	<b>Derwent</b>	<b>IBM TDB</b>
1	372609							
2	79409							
3	50138							
4	43855							
5	2283678							
6	1530592							
7	1							
8	495822							
9	106655							
10	1098703							
11	64							
12	827790							
13	842511							
14	1604							
15	430							
16	733999							
17	504670							
18	36							
19	130549							
20	75498							
21	38146							
22	6724							
23	87461							
24	44578							
25	561856							
26	1070052							
27	6							
28	315264							
29	614094							
30	331							
31	95084							
32	68							
33	297930							

Search Terms	
34	MODULES
35	MULTIPLEXED
36	MULTIPLEXEDS
37	MULTIPLEXER
38	MULTIPLEXERS
39	MULTIPLEXOR
40	MULTIPLEXORS
41	SIGNAL
42	SIGNALLED
43	SIGNALLEDS
44	SIGNALLING
45	SIGNALLINGS
46	SIGNALS
47	TEST
48	TESTED
49	TESTEDS
50	TESTER
51	TESTERS
52	TESTING
53	TESTINGS
54	TESTS
55	TRIGGER
56	TRIGGERED
57	TRIGGEREDS
58	TRIGGERING
59	TRIGGERINGS
60	TRIGGERS
61	((((((MULTIPLEXOR OR MULTIPLEXED OR MULTIPLEXER) SAME (CONTROLLING OR CONTROLLED OR CONTROLLER OR CONTROL)) AND (TRIGGERING OR TRIGGERED OR TRIGGER)) AND (FLIP-FLOP OR (FLOP SAME FLIP))) AND (ANALOG SAME (TESTER OR TESTED OR TESTING OR MEASURE OR MEASURING OR MEASURED OR TEST))) AND (((ENABLE OR ENABLER OR ENABLING OR ENABLE) SAME (TESTER OR TESTED OR TESTING OR TEST)) SAME (CONTROLLING OR CONTROLLED OR CONTROLLER OR CONTROL)) SAME (SIGNALLED OR SIGNALLING OR SIGNAL))) AND (MODULAR OR MODULE))

	<b>Total</b>	<b>US-PGPUB</b>	<b>USPAT</b>	<b>USOCR</b>	<b>EPO</b>	<b>JPO</b>	<b>Derwent</b>	<b>IBM TDB</b>
34	192277							
35	75680							
36	0							
37	95679							
38	33859							
39	10224							
40	3753							
41	1348533							
42	10660							
43	0							
44	43475							
45	66							
46	1048540							
47	838103							
48	486135							
49	0							
50	65551							
51	8128							
52	419239							
53	1050							
54	394174							
55	191928							
56	123414							
57	0							
58	87129							
59	371							
60	77463							
61	135							

U	1	Document ID	Issue Date	Pages	Title	Current OR
1		US 20050052556 A1	20050310	126	Image processor	348/308
2	X	US 20050046718 A1	20050303	126	Image sensor timing circuit	348/308
3	X	US 20050046717 A1	20050303	126	Synchronization protocol	348/308
4	X	US 20050036039 A1	20050217	127	Image sensor with range expander	348/222.1
5	X	US 20050024977 A1	20050203	17	Multiple power levels for a chip within a multi-chip semiconductor package	365/232
6	X	US 20050024524 A1	20050203	127	Image processor with low power mode	348/372
7	X	US 20050024512 A1	20050203	127	Pixel sensor	348/294
8	X	US 20050024511 A1	20050203	127	Image sensor with low-pass filter	348/294
9	X	US 20050024510 A1	20050203	126	Image sensor with digital frame store	348/294
10	X	US 20050010880 A1	20050113	96	Method and user interface for debugging an electronic system	716/4
11	X	US 20050005215 A1	20050106	9	Built-in self-test circuit for phase locked loops, test method and computer program product therefor	714/726
12	X	US 20040249757 A1	20041209	864	Authentication of resources usage in a multi-user environment	705/52
13	X	US 20040243978 A1	20041202	832	Multi-level boot hierarchy for software development on an integrated circuit	717/120
14	X	US 20040227205 A1	20041118	832	Tamper resistant shadow memory	257/499
15	X	US 20040225881 A1	20041111	843	Variant keys	713/161
16	X	US 20040223010 A1	20041111	832	Data rate equalisation to account for relatively different printhead widths	347/6
17	X	US 20040221287 A1	20041104	832	Creation and usage of mutually exclusive messages	718/100
18	X	US 20040201939 A1	20041014	834	Clock trim mechanism for onboard system clock	361/104
19	X	US 20040201647 A1	20041014	832	Stitching of integrated circuit components	347/54
20	X	US 20040199786 A1	20041007	840	Randomisation of the location of secret information on each of a series of integrated circuits	713/200
21	X	US 20040196320 A1	20041007	838	Dead nozzle compensation	347/12
22	X	US 20040193880 A1	20040930	837	Authenticated communication between multiple entities	713/168
23	X	US 20040190092 A1	20040930	244	Scanning device for coded data	358/539

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1			Henderson, Peter Charles Boyd et al.	X							US 20050052556	
2			Yourlo, Zhenya Alexander et al.								US 20050046718	
3			Lapstun, Paul et al.								US 20050046717	
4			Silverbrook, Kia et al.								US 20050036039	
5			Ong, Adrian E.								US 20050024977	
6			Yourlo, Zhenya Alexander et al.								US 20050024524	
7			Moini, Alireza et al.								US 20050024512	
8			Lapstun, Paul et al.								US 20050024511	
9			Lapstun, Paul et al.								US 20050024510	
10	716/11		Schubert, Nils Endric et al.								US 20050010880	
11			Napolitano, Leonardo								US 20050005215	
12			Walmsley, Simon Robert et al.								US 20040249757	
13	717/104; 717/174		Walmsley, Simon Robert								US 20040243978	
14			Walmsley, Simon Robert								US 20040227205	
15			Walmsley, Simon Robert et al.								US 20040225881	
16			Plunkett, Richard Thomas								US 20040223010	
17			Walmsley, Simon Robert								US 20040221287	
18			Shipton, Gary et al.								US 20040201939	
19	347/20		Jackson Pulver, Mark et al.								US 20040201647	
20			Walmsley, Simon Robert et al.								US 20040199786	
21			Walmsley, Simon Robert et al.								US 20040196320	
22			Walmsley, Simon Robert								US 20040193880	
23	358/474		Silverbrook, Kia et al.								US 20040190092	

U	1	Document ID	Issue Date	Pages	Title	Current OR
24	X	US 20040189731 A1	20040930	835	Fixative compensation	347/19
25	X	US 20040189355 A1	20040930	833	Temperature based filter for an on-chip system clock	327/138
26	X	US 20040181729 A1	20040916		Probeless testing of pad buffers on wafer	714/742
27	X	US 20040181303 A1	20040916		Relatively unique ID in integrated circuit	700/115
28	X	US 20040174570 A1	20040909		Variable size dither matrix usage	358/3.13
29	X	US 20040034724 A1	20040219		Digital device configuration and method	710/8
30	X	US 20040025122 A1	20040205		Hardware-based HDL code coverage and design analysis	716/4
31	X	US 20040017224 A1	20040129		Integrated circuit and sensor for imaging	327/51
32	X	US 20030221145 A1	20031127		System and method for testing memory arrays	714/718
33	X	US 20030208708 A1	20031106		Circuit and method for adding parametric test capability to digital boundary scan	714/727
34	X	US 20030182642 A1	20030925		Hardware debugging in a hardware description language	716/4
35	X	US 20030141879 A1	20030731		PICA system timing measurement & calibration	324/601
36	X	US 20030131325 A1	20030710		Method and user interface for debugging an electronic system	716/4
37	X	US 20030093730 A1	20030515		Systems and methods for testing integrated circuits	714/724
38	X	US 20030069724 A1	20030410		Method and system for debugging an electronic system using instrumentation circuitry and a logic analyzer	703/16
39	X	US 20030009715 A1	20030109		Method and apparatus for optimized parallel testing and access of electronic circuits	714/727
40	X	US 20020150156 A1	20021017		Control system methods and apparatus for inductive communication across an isolation barrier	375/238
41	X	US 20020097743 A1	20020725		Integrated digital loop carrier system with virtual tributary mapper circuit	370/463

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
24			Robert Walmisley, Simon et al.								US 20040189731	
25			Walmisley, Simon Robert								US 20040189355	
26			Whetsel, Lee D.									
27	380/44; 700/121		Walmisley, Simon Robert									
28			Plunkett, Richard Thomas et al.									
29	360/69		Bruner, Curtis H. et al.									
30	716/12; 716/17; 716/5; 716/8		Schubert, Nils Endric et al.									
31			Tumer, Tumay O. et al.									
32			Stong, Gayvin E. et al.									
33			Sunter, Stephen K.									
34			Schubert, Nils Endric et al.									
35			Wilsher, Kenneth R.									
36			Schubert, Nils Endric et al.									
37			Halder, Achintya et al.									
38			Schubert, Nils Endric et al.									
39			Ricchetti, Michael et al.									
40	375/258		Calvin, James G.									
41	370/357; 370/386		Baydar, Ertugrul et al.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
42	X		US 20020018513 A1	20020214		Memory	374/178
43	X		US 20010014959 A1	20010816		Probeless testing of pad buffers on wafer	714/724
44	X		US 6823497 B2	20041123		Method and user interface for debugging an electronic system	716/4
45	X		US 6819117 B2	20041116		PICA system timing measurement & calibration	324/601
46	X		US 6791610 B1	20040914		Uncooled focal plane array sensor	348/297
47	X		US 6789030 B1	20040907		Portable data collector and analyzer: apparatus and method	702/77
48	X		US 6731106 B2	20040504		Measuring on-resistance of an output buffer with test terminals	324/158.1
49	X		US 6720812 B2	20040413		Multi-channel integrated circuit	327/170
50	X		US 6618839 B1	20030909		Method and system for providing an electronic system design with enhanced debugging capabilities	716/4
51	X		US 6581191 B1	20030617		Hardware debugging in a hardware description language	716/4
52	X		US RE38140 E	20030610		Dual interleaved DC to DC switching circuits realized in an integrated circuit	363/65
53	X		US 6369855 B1	20020409		Audio and video decoder circuit and system	348/423.1
54	X		US 6333940 B1	20011225		Integrated digital loop carrier system with virtual tributary mapper circuit	370/506
55	X		US 6330977 B1	20011218		Electronic labeling systems and methods and electronic card systems and methods	235/487
56	X		US 6286119 B1	20010904		Delay fault testing with IEEE 1149.1	714/726
57	X		US 6263396 B1	20010717		Programmable interrupt controller with interrupt set/reset register and dynamically alterable interrupt mask for a single interrupt processor	710/263
58	X		US 6246831 B1	20010612		Fluid heating control system	392/486

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>42</b>	374/163; 374/170		Curry, Stephen M. et al.									
<b>43</b>	714/726		Whetsel, Lee D.									
<b>44</b>	716/5		Schubert, Nils Endric et al.									
<b>45</b>	368/120		Wilsher, Kenneth R.									
<b>46</b>	348/294		Butler, Neal R. et al.									
<b>47</b>	702/182		Coyle, Sean et al.									
<b>48</b>	324/765		Whetsel, Lee D.									
<b>49</b>	327/51; 327/69		Tumer, Tumay O et al.									
<b>50</b>	714/735; 716/5; 716/6		Beardslee, John Mark et al.									
<b>51</b>	703/14; 716/17		Schubert, Nils Endric et al.									
<b>52</b>	323/282		Schaffer, Gregory L.									
<b>53</b>	348/558; 370/474		Chauvel, Gerard et al.									
<b>54</b>	370/466; 370/509		Baydar, Ertugrul et al.									
<b>55</b>	235/492		Hass, Steven N. et al.									
<b>56</b>	712/227		Wu, Yuejian et al.									
<b>57</b>	710/266; 710/268		Cottle, Temple D. et al.									
<b>58</b>	219/483; 392/466		Seitz, David E. et al.									

U	1	Document ID	Issue Date	Pages	Title	Current OR
59	X	US 6217213 B1	20010417		Temperature sensing systems and methods	374/178
60	X	US 6199182 B1	20010306		Probeless testing of pad buffers on wafer	714/724
61	X	US 6167011 A	20001226		Processing of disc-drive pickup signals	369/44.41
62	X	US 6122704 A	20000919		Integrated circuit for identifying an item via a serial port	711/100
63	X	US 6118186 A	20000912		Throttle control for small engines and other applications	290/40B
64	X	US 6112275 A	20000829		Method of communicating over a single wire bus between a host device and a module device which measures thermal accumulation over time	711/100
65	X	US 6067648 A	20000523		Programmable pulse generator	714/718
66	X	US 6061809 A	20000509		Process control interface system having triply redundant remote field units	714/11
67	X	US 6055145 A	20000425		Overcurrent protection device with visual indicators for trip and programming functions	361/93.1
68	X	US 6049550 A	20000411		Integrated digital loop carrier system with virtual tributary mapper circuit	370/466
69	X	US 6041028 A	20000321		Dynamic adjustment of disk-drive pickup signals	369/44.34
70	X	US 6036101 A	20000314		Electronic labeling systems and methods and electronic card systems and methods	235/492

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>59</b>	368/202; 374/102		Curry; Stephen M. et al.									
<b>60</b>	326/16		Whetsel; Lee D.									
<b>61</b>	369/44.28; 369/44.29;		Sun, deceased; Kai C. K. et al.									
<b>62</b>	711/162; 712/1		Hass; Steven N. et al.									
<b>63</b>	290/1A; 322/14; 322/15		Scott; Harold C. et al.									
<b>64</b>	710/100; 711/1; 711/4		Curry; Stephen M. et al.									
<b>65</b>	327/170; 327/172; 327/175; 327/176; 327/291		Hunter; Paul R. et al.									
<b>66</b>	714/10; 714/23; 714/43; 714/44		Glaser; Robert S. et al.									
<b>67</b>	361/115; 361/94		Lagree; James L. et al.									
<b>68</b>			Baydar; Ertugrul et al.									
<b>69</b>	369/44.28; 369/59.21		Quan; Thomas A. et al.									
<b>70</b>	235/439; 235/441		Hass; Steven N. et al.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
71	X		US 6018200 A	20000125		Load demand throttle control for portable generator and other applications	290/40B
72	X		US 6016255 A	20000118		Portable data carrier mounting system	361/807
73	X		US 5970226 A	19991019		Method of non-intrusive testing for a process control interface system having triply redundant remote field units	714/25
74	X		US 5953690 A	19990914		Intelligent fiberoptic receivers and method of operating and manufacturing the same	702/191
75	X		US 5886504 A	19990323		Throttle controlled generator system	322/15
76	X		US 5884023 A	19990316		Method for testing an integrated circuit with user definable trace function	714/30
77	X		US 5862315 A	19990119		Process control interface system having triply redundant remote field units	714/11
78	X		US 5847561 A	19981208		Low overhead input and output boundary scan cells	324/158.1
79	X		US 5805619 A	19980908		Method and apparatus for sampled-data partial-response signal timing error detector having zero self-noise	714/814
80	X		US 5784377 A	19980721		Integrated digital loop carrier system with virtual tributary mapper circuit	370/463
81	X		US 5761697 A	19980602		Identifiable modules on a serial bus system and corresponding identification methods	711/100
82	X		US 5627716 A	19970506		Overcurrent protection device	361/93.2

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>71</b>	290/1A; 290/51; 322/14; 322/15		Anderson; William J et al.									
<b>72</b>	361/809		Bolan; Michael L. et al.									
<b>73</b>	714/37; 714/712		Hoy; Robert S. et al.									
<b>74</b>	250/214C; 398/202; 702/193		Lemon; Leon M. et al.									
<b>75</b>	322/46		Scott; Harold C. et al.									
<b>76</b>			Swoboda; Gary L. et al.									
<b>77</b>	700/4; 700/82		Glaser; Robert S. et al.									
<b>78</b>	324/763; 714/727; 714/733		Whetsel; Lee D.									
<b>79</b>	360/51; 375/376		Gardner; Floyd Martin et al.									
<b>80</b>	370/357; 370/386; 379/90.01		Baydar; Ertugrul et al.									
<b>81</b>	710/107; 710/110; 711/1; 711/4		Curry; Stephen M. et al.									
<b>82</b>	361/64; 361/96		Lagree; James L. et al.									

<b>U</b>	<b>1</b>	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
83	X	US 5619066 A	19970408		Memory for an electronic token	257/679
84	X	US 5610826 A	19970311		Analog signal monitor circuit and method	702/117
85	X	US 5576980 A	19961119		Serializer circuit for loading and shifting out digitized analog signals	702/119
86	X	US 5517015 A	19960514		Communication module	235/492
87	X	US 5428769 A	19950627		Process control interface system having triply redundant remote field units	700/4
88	X	US 5410621 A	19950425		Image processing system having a sampled filter	382/260
89	X	US 5333239 A	19940726		Learning process system for use with a neural network structure data processing apparatus	706/25
90	X	US 5325315 A	19940628		Process for auto calibration of a microprocessor based overcurrent protective device and apparatus	702/105
91	X	US 5260554 A	19931109		System for automatically reading symbols, such as bar codes, on objects which are placed in the detection zone of a symbol reading unit, such as a bar code scanner	235/462.31
92	X	US 5237161 A	19930817		System for automatically reading symbols, such as bar codes, on objects which are placed in the detection zone of a symbol reading unit, such as a bar code scanner	235/462.31
93	X	US 5130988 A	19920714		Software verification by fault insertion	714/727
94	X	US 5124942 A	19920623		Machine interface with cyclically displayed hierarchical menus and user selection of menu items by actuation of a single switch	715/713

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
83	235/492; 257/730; 257/732		Curry; Stephen M. et al.									
84			Whetsel; Lee D.									
85	341/100; 341/101; 714/712		Whetsel; Lee D.									
86	257/678; 257/679; 257/730; 257/732		Curry; Stephen M. et al.									
87	710/240; 714/1; 714/11		Glaser; Robert S. et al.									
88	382/263; 708/300		Hyatt; Gilbert P.									
89			Watanabe; Nobuo et al.									
90			Engel; Joseph C. et al.									
91	235/470; 250/222.1; 250/568		Grodevant; Scott R.									
92	250/222.1; 250/568		Grodevant; Scott R.									
93	714/703; 716/4		Wilcox; Philip S. et al.									
94	715/784; 715/840; 715/841		Nielsen; Wyn Y. et al.									

U	1	Document ID	Issue Date	Pages	Title	Current OR
95	X	US 5081297 A	19920114		Software reconfigurable instrument with programmable counter modules reconfigurable as a counter/timer, function generator and digitizer	710/104
96	X	US 5061841 A	19911029		Apparatus and methods for controlling a welding process	219/130.01
97	X	US 5032789 A	19910716		Modular/concurrent board tester	324/754
98	X	US 5019906 A	19910528		Time base corrector memory arrangement and memory control	348/497
99	X	US 5014315 A	19910507		Digital telephone system	379/93.06
100	X	US 5005142 A	19910402		Smart sensor system for diagnostic monitoring	702/183
101	X	US 4943702 A	19900724		Apparatus and methods for controlling a welding process	219/124.34
102	X	US 4933633 A	19900612		Computer controlled energy monitoring system	324/142
103	X	US 4763203 A	19880809		Time base corrector with accurate timing corrector control	386/20
104	X	US 4750111 A	19880607		Computer system for processing analog and digital data	710/58
105	X	US 4739404 A	19880419		Apparatus and methods for controlling a welding process	348/719
106	X	US 4733294 A	19880322		Time base corrector memory arrangement and memory control	348/498
107	X	US 4646255 A	19870224		Gyro simulator	703/8
108	X	US 4595820 A	19860617		Apparatus and methods for controlling a welding process	219/137PS

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>95</b>	702/124; 702/189		Lebel; Joseph A. et al.									
<b>96</b>			Richardson; Richard W.									
<b>97</b>	324/73.1; 714/724		Firooz; Kamran et al.									
<b>98</b>	348/715; 386/85		Wesolowski; Jan S.									
<b>99</b>	370/434; 379/334; 380/257		Polyak; Mark U. et al.									
<b>100</b>	376/245; 700/9; 73/117.2		Lipchak; John B. et al.									
<b>101</b>	219/130.01		Richardson; Richard W.									
<b>102</b>	324/116; 324/141; 702/62		Allgood; Marvin D.									
<b>103</b>	360/26		Oldershaw; Reginald W. et al.									
<b>104</b>			Crosby, Jr.; Edward D. et al.									
<b>105</b>	348/90		Richardson; Richard W.									
<b>106</b>	348/715; 386/20		Wesolowski; Jan S.									
<b>107</b>	701/3; 703/6		Ebert; Philip C. et al.									
<b>108</b>	219/130.01; 219/130.21; 219/130.51		Richardson; Richard W.									

	<b>U</b>	<b>1</b>	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
<b>109</b>	X		US 4571724 A	19860218		System for testing digital logic devices	714/724
<b>110</b>	X		US 4568934 A	19860204		Computer controlled energy monitoring system	340/870.02
<b>111</b>	X		US 4566093 A	19860121		Continuity check tone detector for use with a digital telecommunication system	370/248
<b>112</b>	X		US 4564900 A	19860114		Multiprocessor computer system	709/212
<b>113</b>	X		US 4519071 A	19850521		Phase-locked loop and clock circuit for a line switch	370/503
<b>114</b>	X		US 4477847 A	19841016		Television signal disc drive recorder	360/60
<b>115</b>	X		US 4476423 A	19841009		Motor control apparatus with motor starts per time period limiter	318/800
<b>116</b>	X		US 4467260 A	19840821		Motor control apparatus with rotor heating protection	318/800
<b>117</b>	X		US 4466106 A	19840814		Frequency shift keyed modem chip set	375/223
<b>118</b>	X		US 4461986 A	19840724		Motor control apparatus with phase related detector	318/728
<b>119</b>	X		US 4456867 A	19840626		Motor control apparatus with input validator	318/778

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>109</b>	714/738		Belmondo; Victor E. et al.									
<b>110</b>	340/870.05; 340/870.38; 700/291; 702/62; 705/412		Allgood; Marvin D.									
<b>111</b>	370/252; 379/16; 379/22.03; 379/27.08		Diaz; Felix V.									
<b>112</b>	709/213; 709/215		Smitt; Asbjorn									
<b>113</b>	370/540;		Miller; Robert L.									
<b>114</b>	370/545											
<b>115</b>	386/125; 386/94		Knight; Howard W. et al.									
<b>116</b>	318/782; 361/29		Mallick, Jr.; George T. et al.									
<b>117</b>	318/434; 318/473; 318/783; 361/23; 361/24; 700/170; 700/177; 700/79		Mallick, Jr.; George T. et al.									
<b>118</b>	329/303; 375/334		Serrano; Arthur L.									
<b>119</b>	361/33; 361/86		Maynard; Pamela M. et al.									
			Mallick, Jr.; George T. et al.									

U	1	Document ID	Issue Date	Pages	Title	Current OR
		US 4449247 A	19840515		Local orderwire facility for fiber optic communication system	455/9
120	X					
121	X	US 4408285 A	19831004		Vibration analyzing apparatus and method	702/56
122	X	US 4366350 A	19821228		Control system for telephone switching system	379/29.07
123	X	US 4361870 A	19821130		Microprocessor circuit providing vehicle parameter test data	701/33
124	X	US 4347618 A	19820831		Apparatus for processing weather radar information	375/259
125	X	US 4276451 A	19810630		Control system for telephone switching system	379/15.05
126	X	US 4270150 A	19810526		Video frame storage recording and reproducing apparatus	386/86
127	X	US 4224690 A	19800923		High speed parallel scanning means for testing or monitoring the assembly of multiwire harnesses	714/736
128	X	US 4099237 A	19780704		Programmable turbine speed controller	700/290
129	X	US 4085438 A	19780418		Digital sputblower control systems and methods therefor	700/278
130	X	US 4070565 A	19780124		Programmable tester method and apparatus	714/734

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	340/505; 340/531; 375/227; 380/256; 380/257; 380/46; 398/11; 398/16; 398/37		Waschka, Jr.; George A.									
<b>120</b>												
<b>121</b>	73/570; 73/659		Siisson; Edwin D. et al.									
<b>122</b>	379/18		Lee; Ernest O. et al.									
<b>123</b>	73/117.2		D'Agostini; Armond N. et al.									
<b>124</b>	342/26D; 348/442; 455/9; 702/3		Kavouras; Stephen P. et al.									
<b>125</b>	340/825.01; 379/269		Beebe; Otto W. et al.									
<b>126</b>	360/22; 360/63; 360/78.04; 386/92		Diemann; Joachim P. et al.									
<b>127</b>	324/540; 324/73.1; 702/117		Rockwell; Kenneth N.									
<b>128</b>			Zitelli; William E. et al.									
<b>129</b>	122/392; 15/318.1		Butler; Robert Gerald									
<b>130</b>	702/120; 716/4		Borrelli; Ronald N.									

	<b>U</b>	<b>1</b>	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current Or</b>
<b>131</b>	X		US 4059729 A	19771122		Method and system for selectively accessing multiplexed data transmission network for monitoring and testing of the network	370/249
<b>132</b>	X		US 4053946 A	19771011		Modular programmable digital scan converter	345/501
<b>133</b>	X		US 3832712 A	19740827		DOPPLER SIGNAL SIMULATOR	342/168
<b>134</b>	X		US 3795771 A	19740305		PASSENGER ENTERTAINMENT/PASSENGER SERVICE AND SELF-TEST SYSTEM	370/242
<b>135</b>	X		US 3681758 A	19720801		DATA ACQUISITION UNIT WITH MEMORY	714/25

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<b>131</b>	714/716		Eddy; Wesley L. et al.									
<b>132</b>	703/24; 703/25; 703/27		Opittek; Eugene W. et al.									
<b>133</b>	342/171		Goetz; Philip J. et al.									
<b>134</b>	340/825.24; 381/82; 381/86; 725/76		Gundersen; James L. et al.									
<b>135</b>	714/734; 902/1		Oster; Stanley et al.									

	Search Terms
1	INABA-KENJI
2	INABA-KENJIS
3	MIURA-TOSHIYUKI
4	MIURA-TOSHIYUKTS
5	MIYAZAKI-MASASHI
6	MIYAZAKI-MASASHIS
7	((INABA-KENJI.IN.) OR (MIURA-TOSHIYUKI.IN.)) OR (MIYAZAKI-MASASHI.IN.))

	<b>Total</b>	<b>US-PGPUB</b>	<b>USPAT</b>	<b>USOCR</b>	<b>EPO</b>	<b>JPO</b>	<b>Denavit</b>	<b>IBM TDB</b>
1	3							
2	0							
3	4							
4	0							
5	8							
6	0							
7	15							

	<b>U</b>	<b>1</b>	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
<b>1</b>			US 20050068152 A1	20050331	14	Writer, reader, and examining method	340/5.8
<b>2</b>	X		US 20040177303 A1	20040909	8	Analog-hybrid ic test system	714/741
<b>3</b>	X		US D500282 S	20041228	5	Aircraft	D12/319
<b>4</b>	X		US D500281 S	20041228	8	Aircraft	D12/319
<b>5</b>	X		US 6306481 B1	20011023	11	Multilayer circuit board having insulating layer with via-holes	428/137
<b>6</b>	X		US 6190834 B1	20010220	16	Photosensitive resin composition, and multilayer printed circuit board using the same	430/284.1
<b>7</b>	X		US 5951859 A	19990914	18	Washing and disinfecting method and apparatus for artificial dialyzer using acid water electrolytically made	210/192

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1	340/10.51; 705/26	Umeshara, Junichi et al.	X								US 20050068152	
2		Miura, Toshiyuki									US 20040177303	
3		Aruga; Hiroyasu et al.									US D500282	
4		Aruga; Hiroyasu et al.									US D500281	
5	428/209; 428/413; 428/418; 428/901; 430/280.1; 522/170; 522/25; 522/83	Amou; Satoru et al.									US 6306481	
6	430/280.1; 522/31; 522/90; 522/905; 522/92; 522/94; 522/95; 525/526; 525/528; 525/530	Naruhara; Masatoshi et al.									US 6190834	
7	204/242; 204/252; 204/278.5; 210/205; 210/321.69; 210/321.71; 210/900; 422/44	Miura; Toshiyuki et al.									US 5951859	

U	1	Document ID	Issue Date	Pages	Title	Current OR
		US 5919603 A	19990706	8	Process for producing printed wiring board	430/281.1
8	X	US 5914216 A	19990622	11	Multilayer circuit board and photosensitive resin composition used therefor	430/280.1
9	X	US 5759489 A	19980602	18	Washing and disinfecting method and apparatus for artificial dialyzer using acid water electrolytically made	422/28
10	X	US 5712080 A	19980127	14	Method for manufacturing printed circuit board	430/315
11	X	US 5438751 A	19950808	8	Process for producing printed wiring board	29/847
12	X	US 5294291 A	19940315	11	Process for the formation of a conductive circuit pattern	216/12
13	X	US 4885873 A	19891212	6	Abrading apparatus for spherically shaping workpieces	451/272
14	X	US 3617158 A	19711102	3	MULTISTAGE ROTARY COMPRESSOR	418/11
15	X					

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8	522/117; 522/121; 522/173; 522/178; 522/182; 522/183; 526/302; 526/308; 526/313; 526/320; 526/323.2		Miyazaki; Masashi et al.								US 5919603	
9	430/311; 522/170; 522/25; 522/83		Amou; Satoru et al.								US 5914216	
10	134/22.11; 205/500; 205/626; 205/746; 210/636; 210/748		Miura; Toshiyuki et al.								US 5759489	
11	430/275.1; 430/277.1; 430/954		Satsu; Yuichi et al.								US 5712080	
12	29/830; 430/315		Miyazaki; Masashi et al.								US 5438751	
13	216/100; 216/13; 216/83		Akahoshi; Haruo et al.								US 5294291	
14	451/276; 451/42		Miyazaki; Masashi								US 4885873	
15	418/245		Yamamoto; Masakazu et al.								US 3617158	